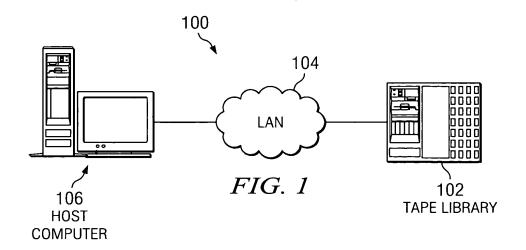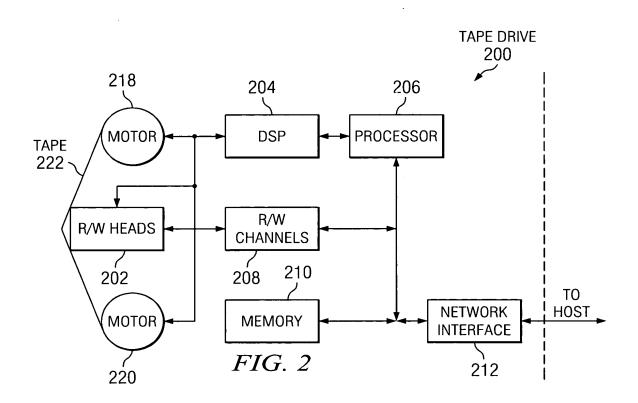2003-022-TAP
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Parallel Channels in a MultiChannel Tape Drive

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100

104

LAN

FIG. 1

106
HOST
COMPUTER

102
TAPE LIBRARY

TAPE DRIVE
200

218

204

206

MOTOR

DSP

PROCESSOR

TAPE
222

R/W HEADS

R/W
CHANNELS

202

208   210

MOTOR

MEMORY

NETWORK
INTERFACE

TO
HOST

220

FIG. 2

212

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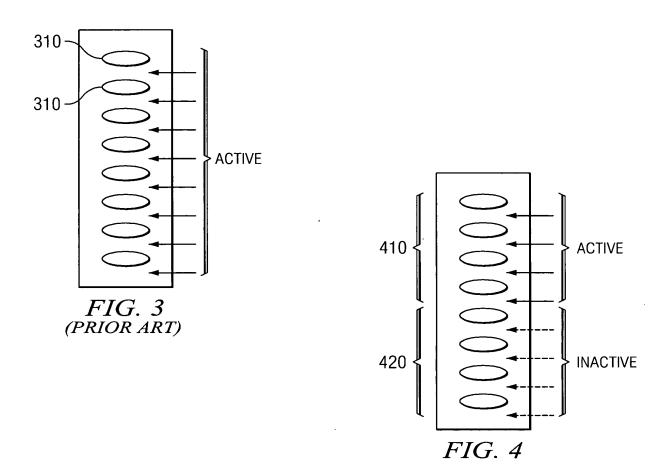Parallel Channels in a MultiChannel Tape Drive
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310

310

ACTIVE

FIG. 3
(PRIOR ART)

410

ACTIVE

420

INACTIVE

FIG. 4

A   B

ALL INACTIVE

ALL (OR PARTIALLY
ALL) ACTIVE

510   **FIG. 5**   520

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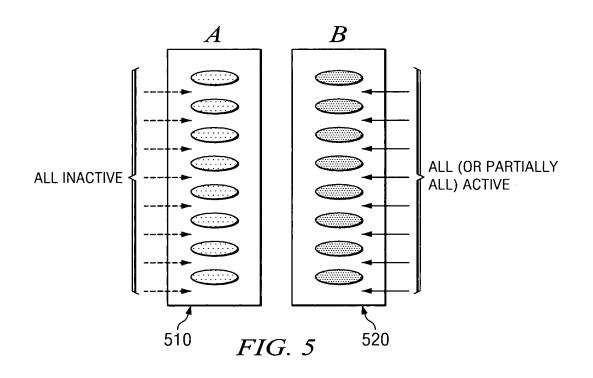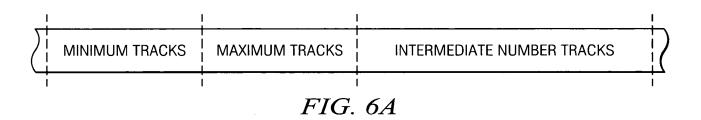| MINIMUM TRACKS	MAXIMUM TRACKS	INTERMEDIATE NUMBER TRACKS

FIG. 6A

ALL MINIMUM, MAXIMUM, OR INTERMEDIATE NUMBER TRACKS

FIG. 6B

MAXIMUM TRACKS	INTERMEDIATE NUMBER TRACKS	ALL MINIMUM, MAXIMUM, OR INTERMEDIATE NUMBER TRACKS
MINIMUM TRACKS	MAXIMUM TRACKS	

FIG. 6C